

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**



Serial No.: 10/699,932

Filed: November 4, 2003

Group Art Unit: 2818

Examiner: Lam, David

Atty Dkt No.: Dahlberg 1-2

Our Ref.: 20-324

IN RE PATENT APPLICATION OF:

**DAHLBERG**

TITLE: **PROGRAMMABLE DATA STROBE OFFSET WITH DLL FOR  
DOUBLE DATA RATE (DDR) RAM MEMORY**

February 7, 2005

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Responsive to the *Ex Parte Quayle* Office Action dated November 30, 2004, please enter the following remarks in the subject application: